

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

Shinji SAITO, et al.

GROUP: 2818

SERIAL NO: 10/574,850

FILED: April 6, 2006

EXAMINER: HUYNH, ANDY

FOR: SEMICONDUCTOR DEVICE, METHOD FOR FABRICATING AN
ELECTRODE, AND METHOD FOR MANUFACTURING A
SEMICONDUCTOR DEVICE

SUBMISSION OF SUPPLEMENTAL APPLICATION DATA SHEET

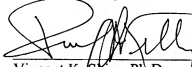
Office of Initial Patent Examination
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant(s) submit herewith a Supplemental Application Data Sheet for the above-identified application for the purpose of correcting the Domestic Priority information.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, L.L.P.
Norman F. Oblon



Vincent K. Shier, Ph.D.
Registration No. 50,552

Customer Number

22850

Tel. (703) 413-3000
Fax. (703) 413-2220
(OSMMN 07/05)

Paul H. Miles
Registration No. 58,014